



#8 ELECTION
7/18/03
Mullish

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

NAKAMURA, Shunji

Group Art Unit: 2811

Serial No.: 10/003,304

Examiner: NADAV, Ori

Filed: December 6, 2001

P.T.O. Confirmation No.: 7007

For: SEMICONDUCTOR MEMORY DEVICE AND METHOD FOR FABRICATING THE
SAME

RESPONSE TO THE RESTRICTION REQUIREMENT
DATED June 25, 2003

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: July 14, 2003

Sir:

This paper is submitted in response to the Official Action dated **June 25, 2003**.

In the Action, restriction is required between Group (I), Claims 2, 4-14 and 29-31; and Group (II),

Claim 28.

Applicant hereby elects the subject matter of Group (I), Claims 2, 4-14 and 29-31 for prosecution in this application. This election is made without traverse, it being understood that the applicant's rights to the filing of a divisional application directed to the non-elected subject matter under 35 USC 120 and 35 USC 121 are retained.

In the event that this paper is not timely filed, applicant hereby petitions for an appropriate extension of time. The fee for any such extension may be charged to our Deposit Account No. 01-2340.

RECEIVED
JUL 15 2003
TECHNOLOGY CENTER 2800

Serial No. 10/003,304

In the event any additional fees are required in connection with this response, please charge our
Deposit Account No. 01-2340.

Respectfully submitted,

ARMSTRONG, WESTERMAN & HATTORI, LLP



Sadao Kinashi
Attorney for Applicant
Reg. No. 48,075

SK/fs
Atty. Docket No. **960456B**
Suite 1000
1725 K Street, N.W.
Washington, D.C. 20006
(202) 659-2930



23850

PATENT TRADEMARK OFFICE